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(Use as many sheets as necessary)

Sheet	1	of	3
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Complete if Known

Application Number	09/650,850
Filing Date	08/29/2000
First Named Inventor	James D. Barnette
Art Unit	2634
Examiner Name	Kevin Kim
Attorney Docket Number	J. BARNETTE 1

U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

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Date
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10/27/04

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Substitute for H.R. 49/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	2	of	3
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Application Number	09/650,850
Filing Date	08/29/2000
First Named Inventor	James D. Barnette
Art Unit	2634
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Attorney Docket Number	J. BARNETTE 1

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

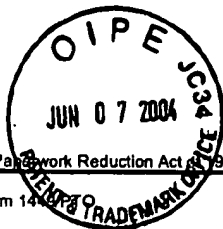
[illegible]

Examiner Signature	<i>Kevin J. Keri</i>	Date Considered	<i>10/29/04</i>
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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 3 of 3

Application Number	09/650,850
Filing Date	08/29/2000
First Named Inventor	James D. Barnette
Art Unit	2634
Examiner Name	Kevin Kim
Attorney Docket Number	J. BARNETTE 1

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
K		Floyd M. Gardner "Interpolation in Digital Modems - Part I: Fundamentals" IEEE Transactoins on Communications, Vol. 41, No. 3, March 1993; pp. 501-507	
K		Lars Erup, et al. "Interpolation in Digital Modems - Part II: Implementation and Performance" IEEE Transactions on Communications, Vol. 41, No. 6, June 1993; pp. 998-1008	
E		James. D. Barnette, et al.; U.S. Patent Application Serial No. 09/652,116, filed 08/29/2000 entitled "Resampler for a bit Pump and Method of Resampling a Signal Associated Therewith"; pp. 1-59; 6 drawing sheets	
K		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,854, filed 08/29/2000 entitled "Separation Circuit for an Echo Canceling System and Method of Operating the Same"; pp. 1-67; 6 drawing sheets	
K		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,853, filed 08/29/2000 entitled "Echo Canceling System for a Bit Pump and Method of Opearting the Same"; pp. 1-60; 5 drawing sheets	
K		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,851, filed 08/29/2000 entitled "Filter Circuit for a Bit Pump and Method of Configuring the Same" pp. 1-57; 7 drawing sheets	

Examiner Signature	Kevin Kim	Date Considered	10/24/04
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